

FIG. 3A

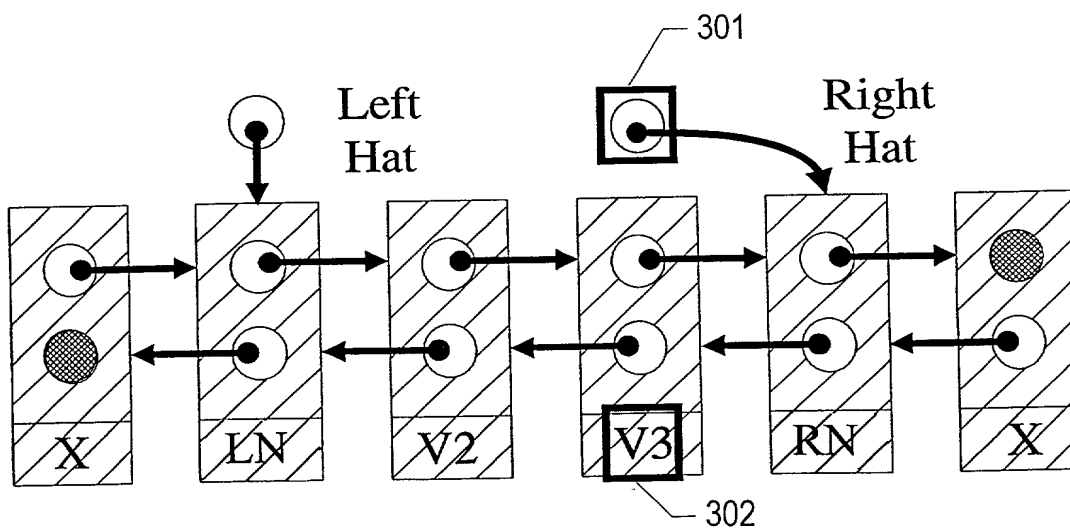


FIG. 3B

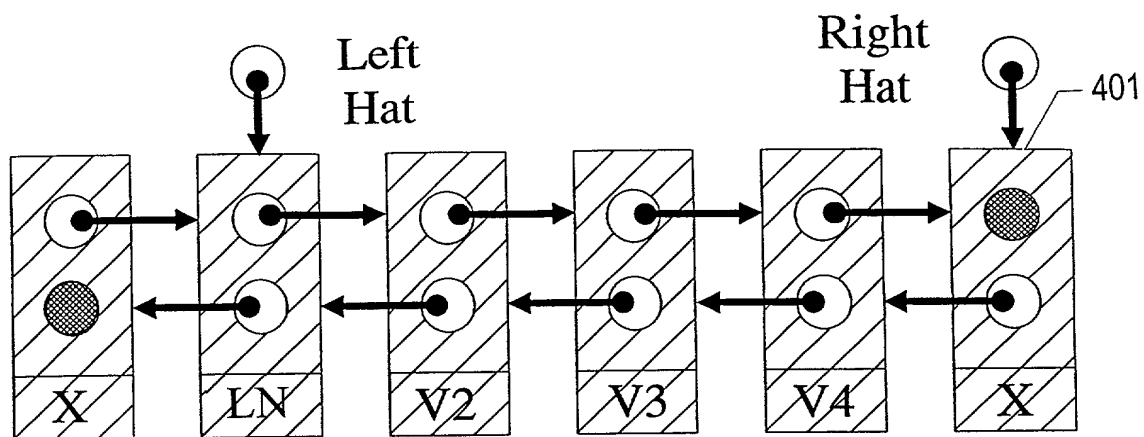


FIG. 4

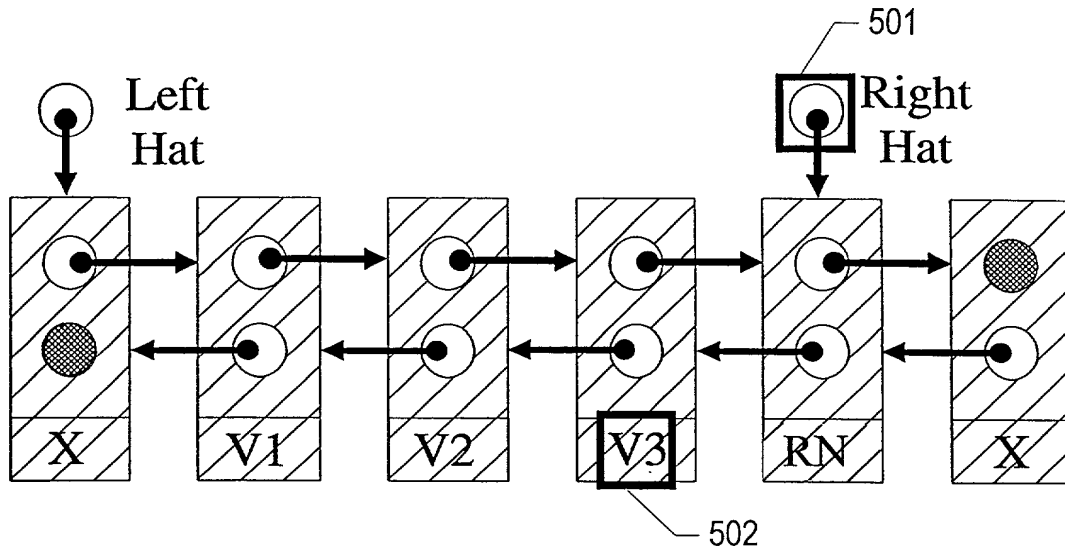


FIG. 5A

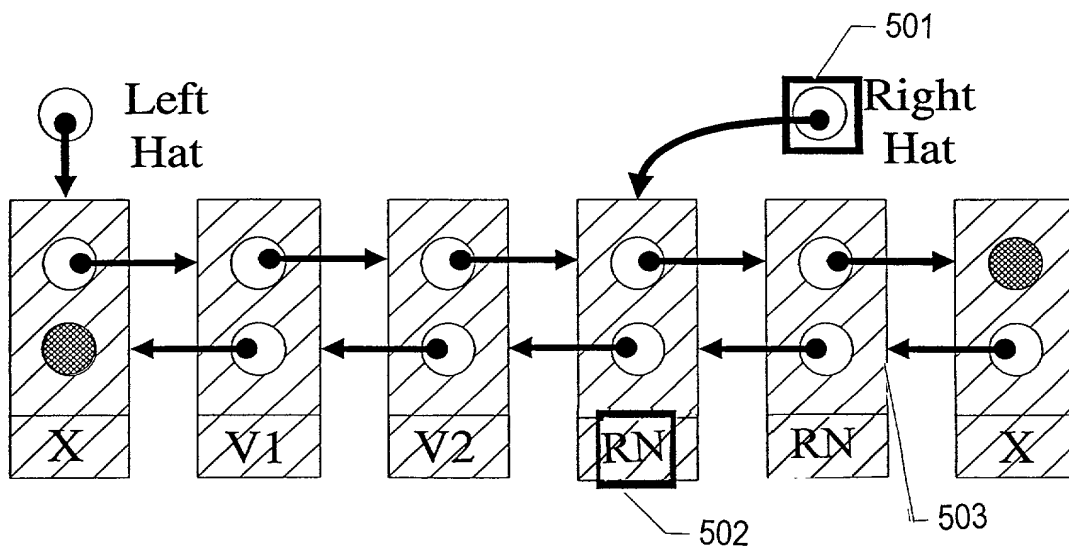


FIG. 5B

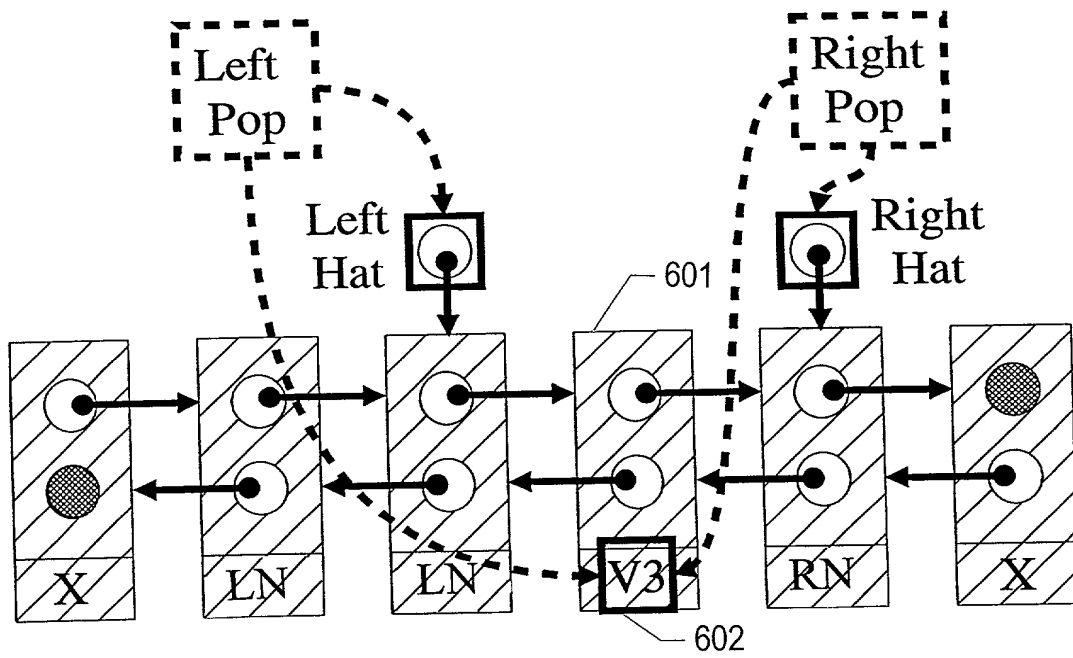


FIG. 6

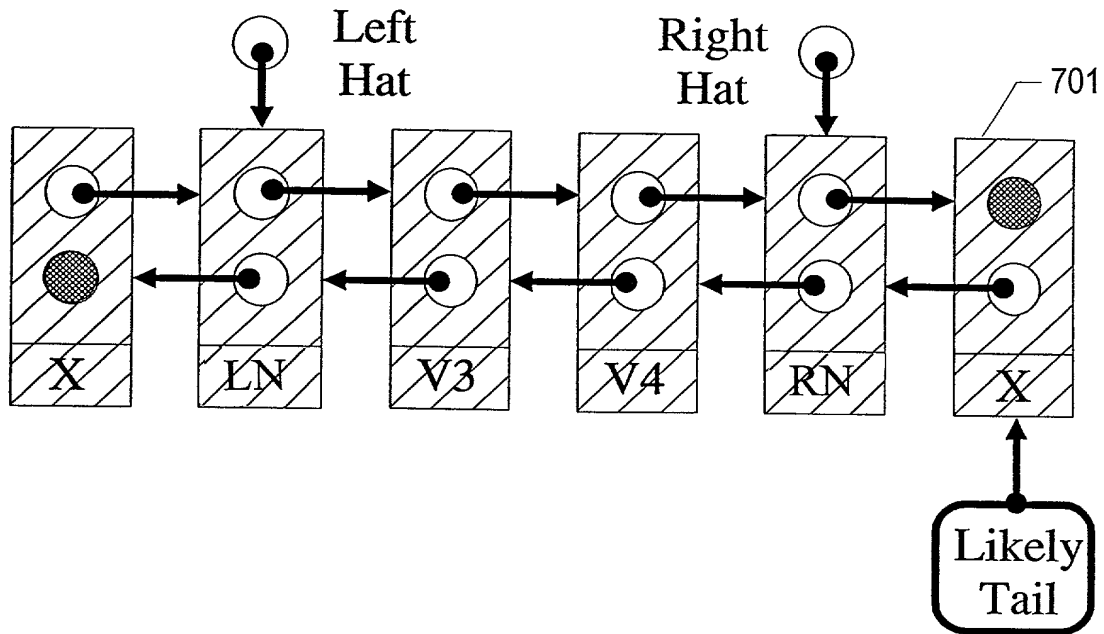


FIG. 7

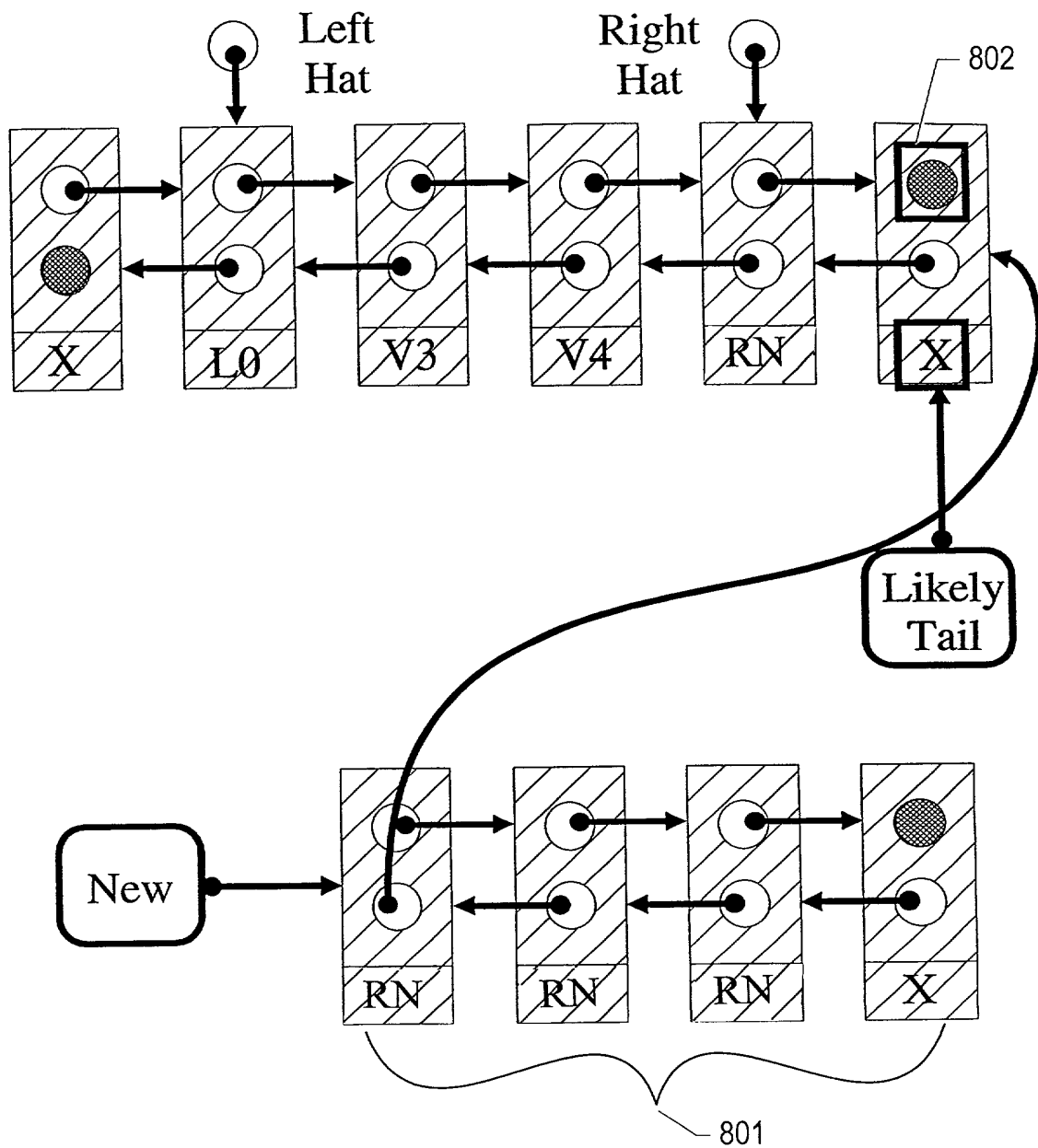


FIG. 8

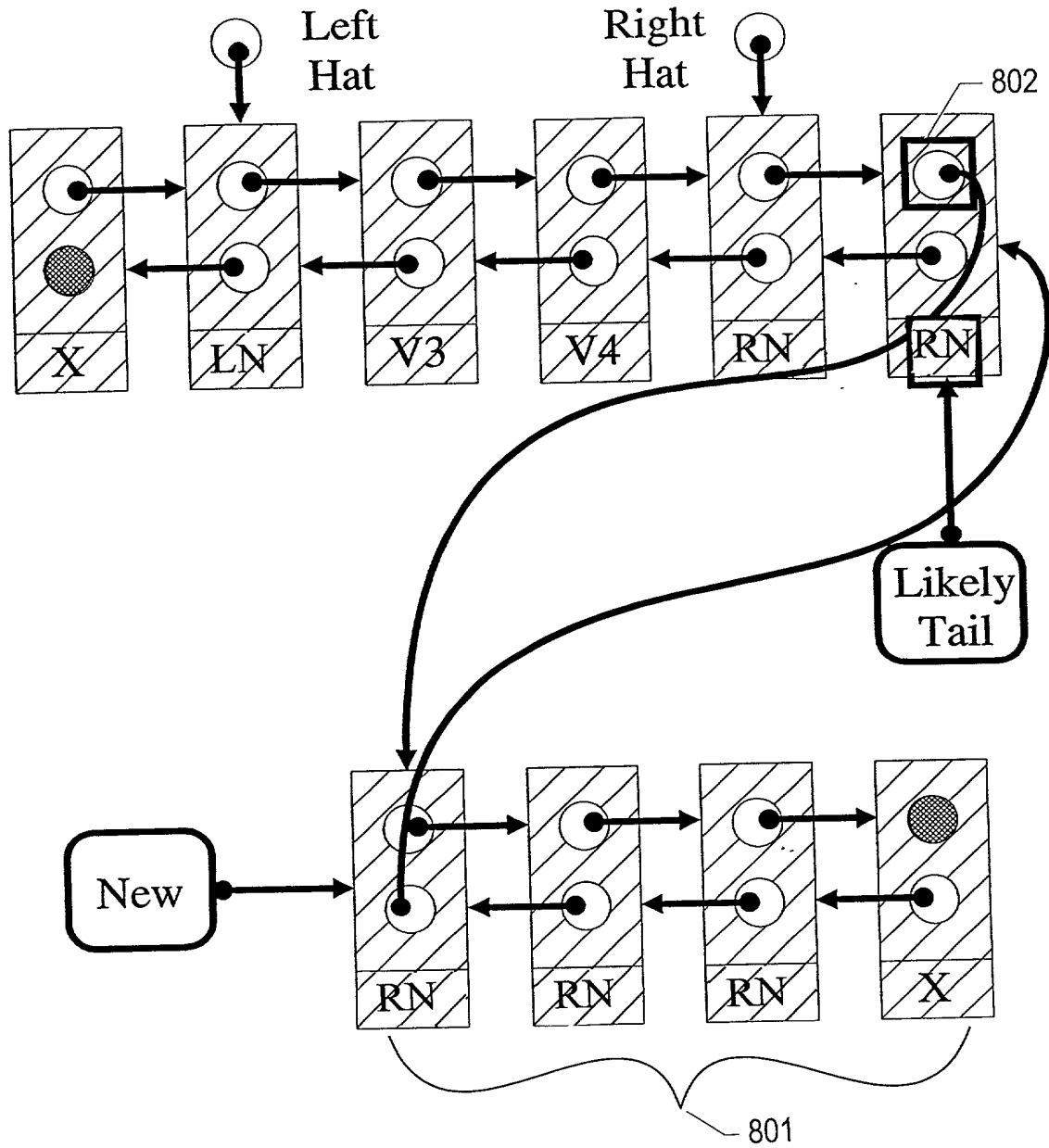


FIG. 9



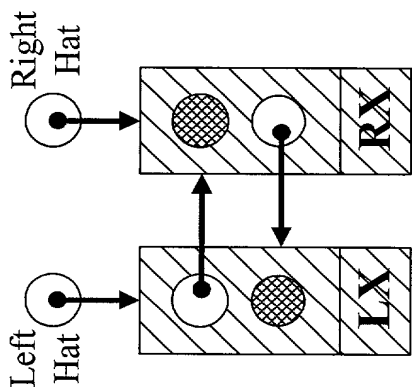


FIG. 10A

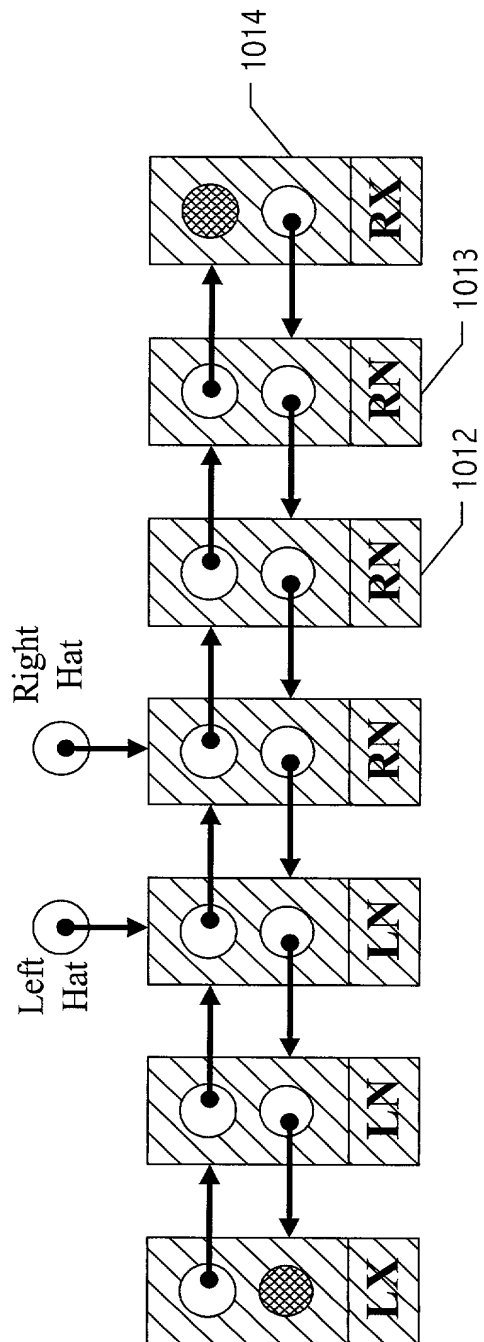
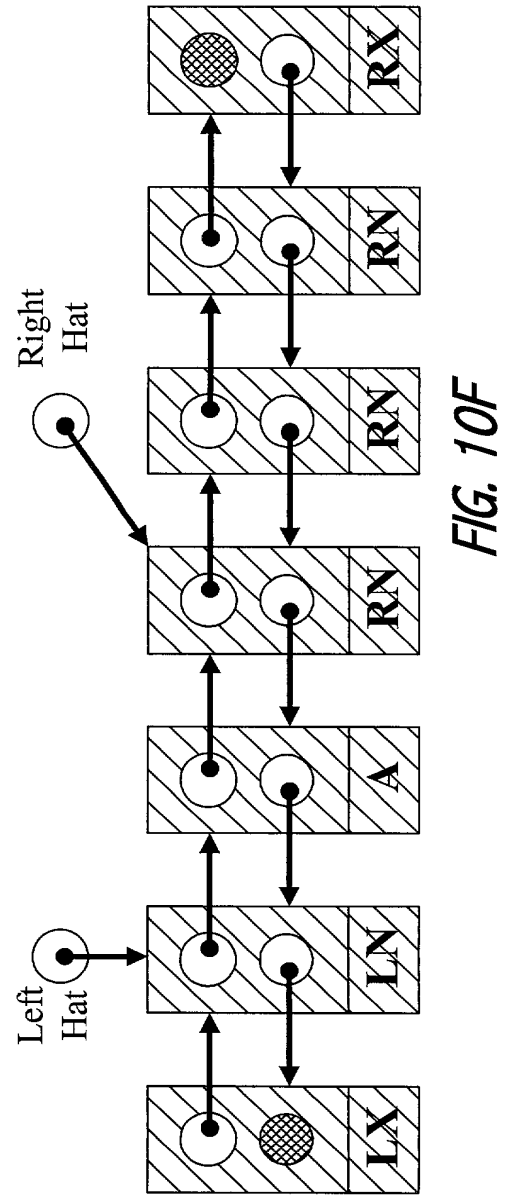
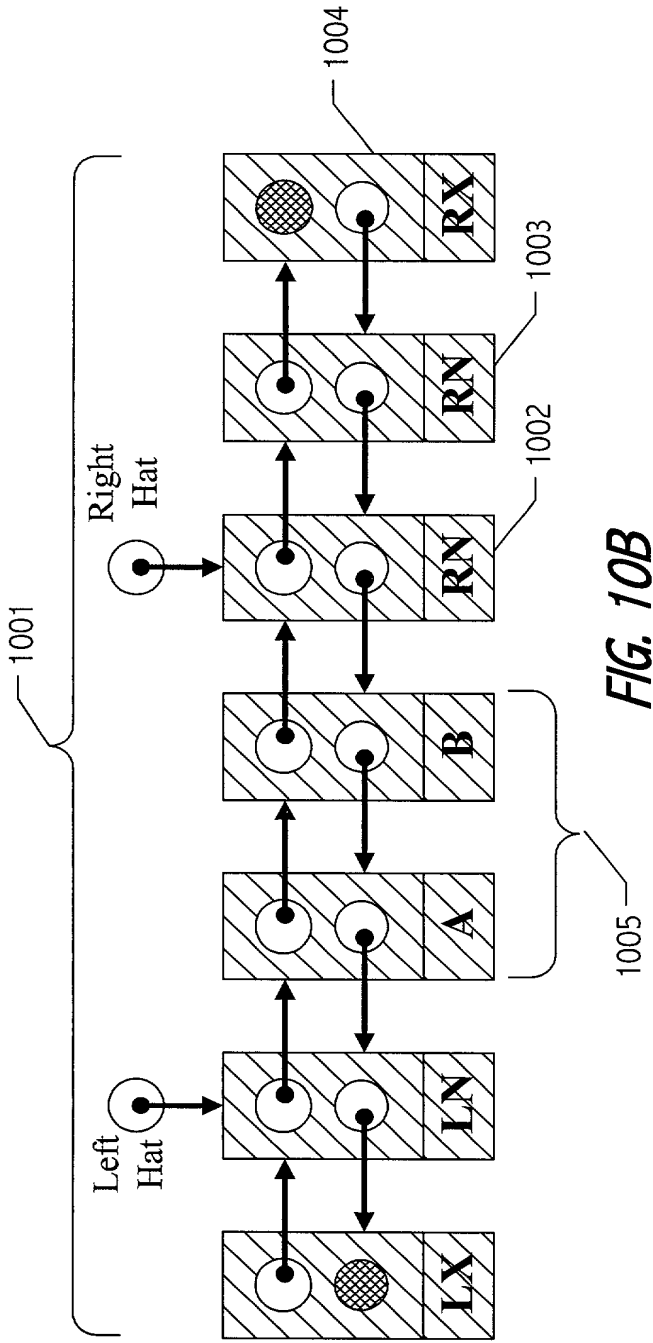


FIG. 10C



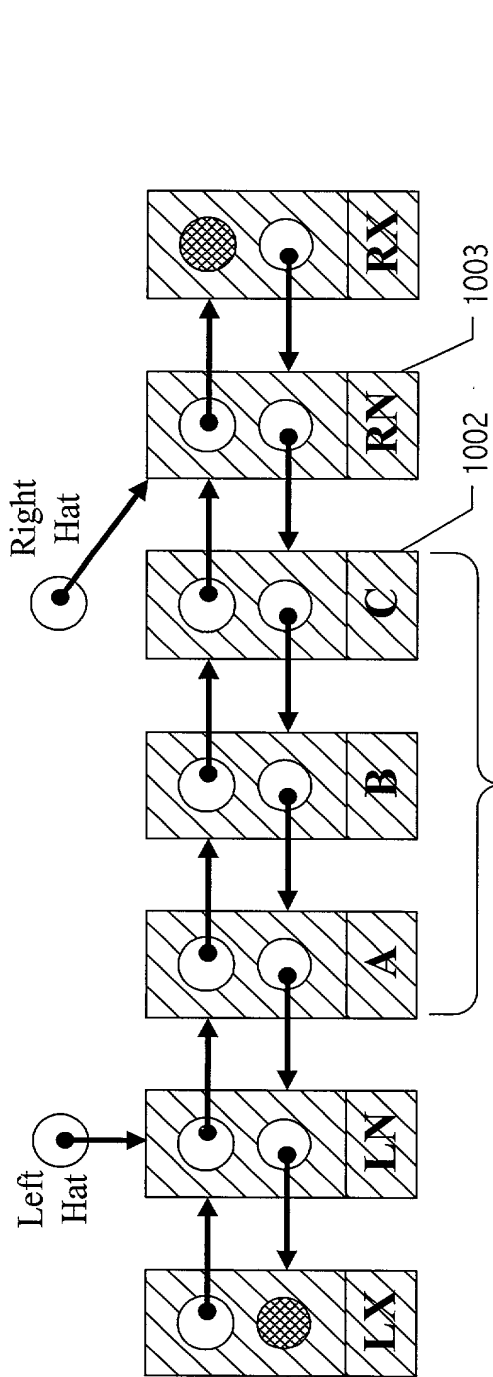


FIG. 10D

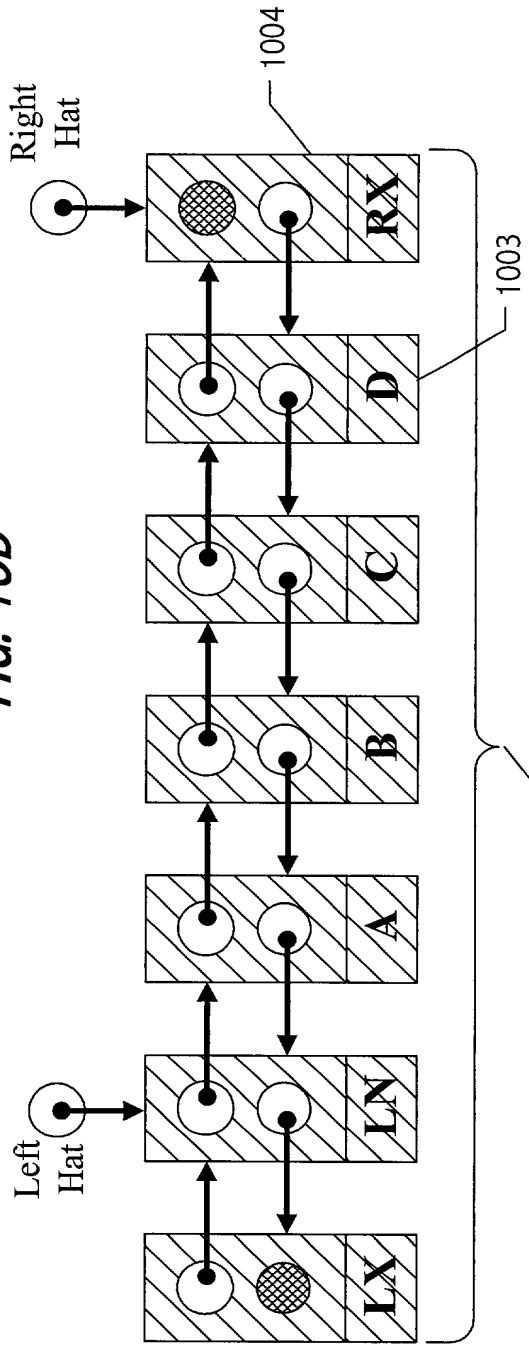


FIG. 10E

FIG. 11

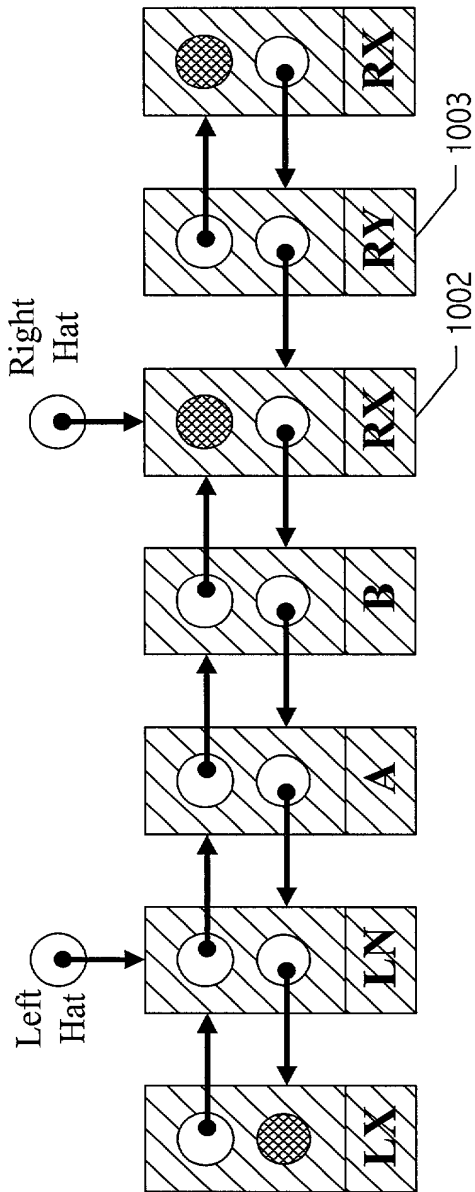
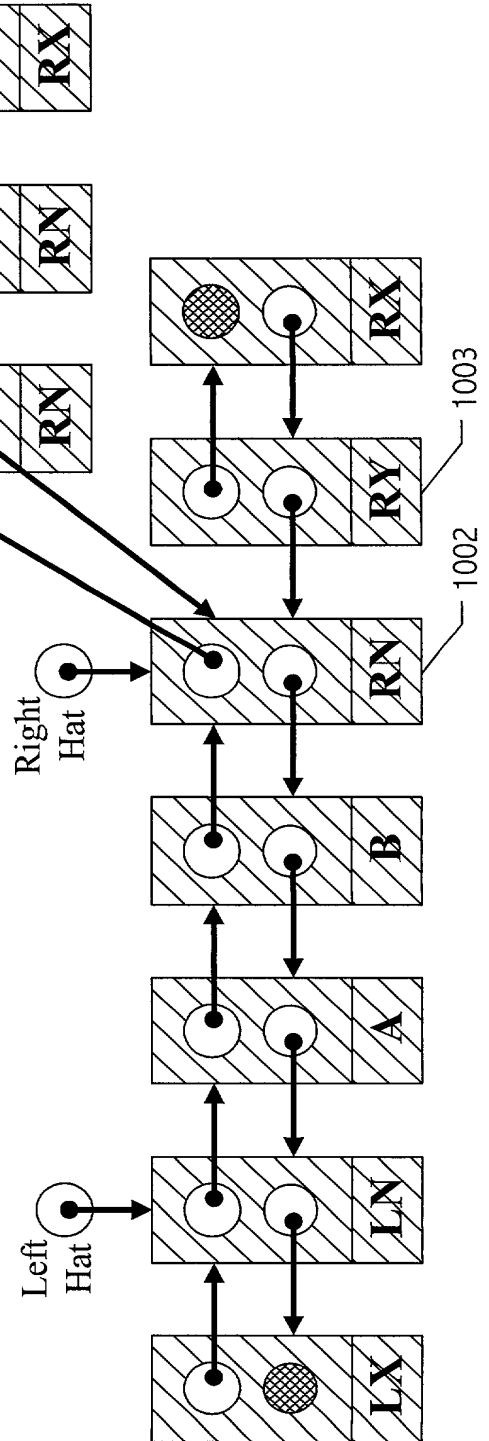


FIG. 12



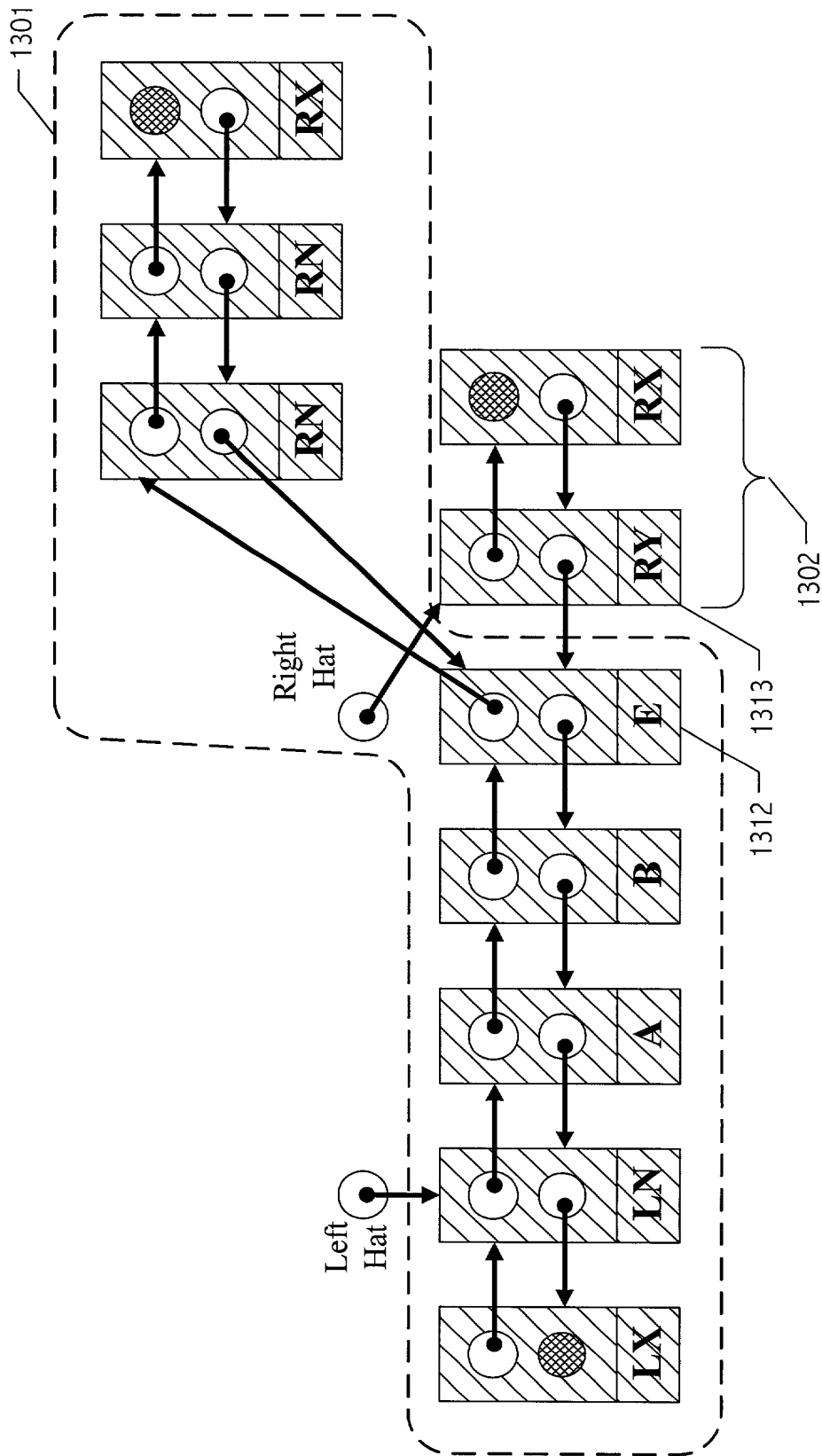


FIG. 13

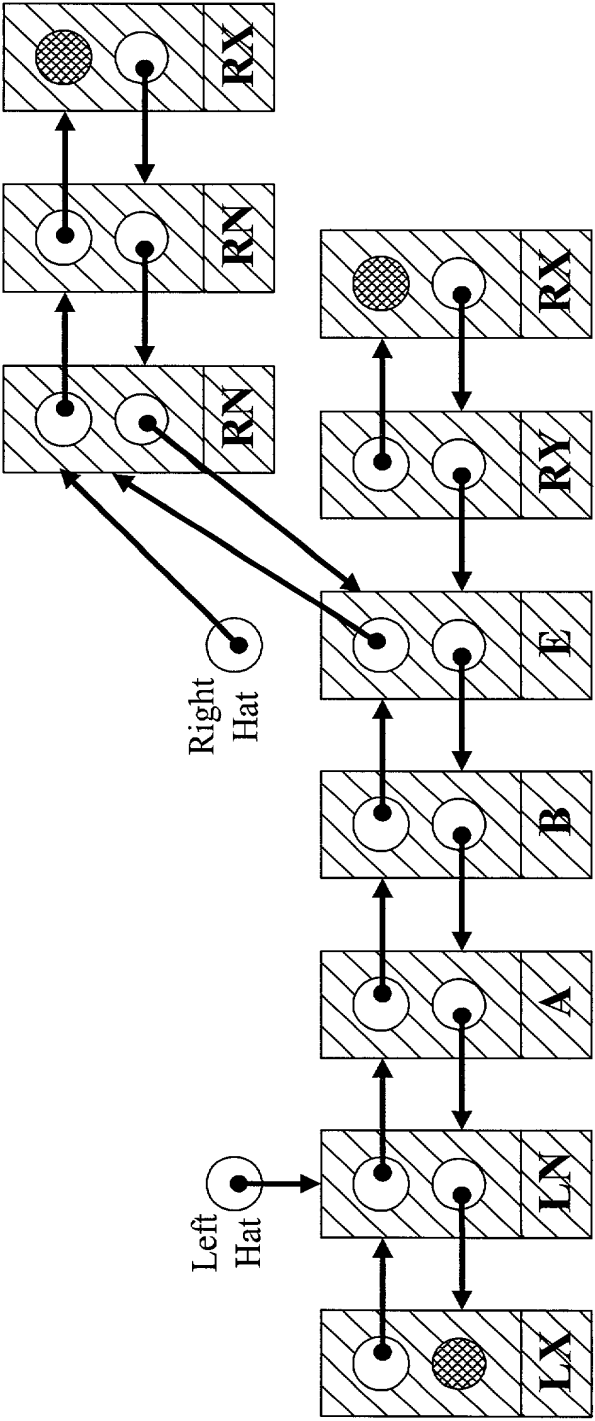


FIG. 14

FIG. 14 is a schematic diagram of a neural network architecture.